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\*\* CONTINUING DATA \*\*\*\* *Y/21 LV*

This application is a CON of PCT/EP02/05253 05/13/2002

\*\* FOREIGN APPLICATIONS \*\*\*\* *Y/21 LV*

GERMANY 10123509.7 05/15/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/23/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>Examiner's Signature</i> <i>LV</i>	GERMANY	4	7	1

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## TITLE

Method for the planarization of a semiconductor structure

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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